

2818
94/18

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Krishna Seshan	§	Art Unit:	2818
Serial No.:	10/022,700	§	Examiner:	Mai Huong C. Tran
Filed:	October 30, 2001	§	Assignee:	Intel Corporation
Title:	POWER GRIDDING SCHEME	§	Docket No.	ITL.0655US (P12430)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

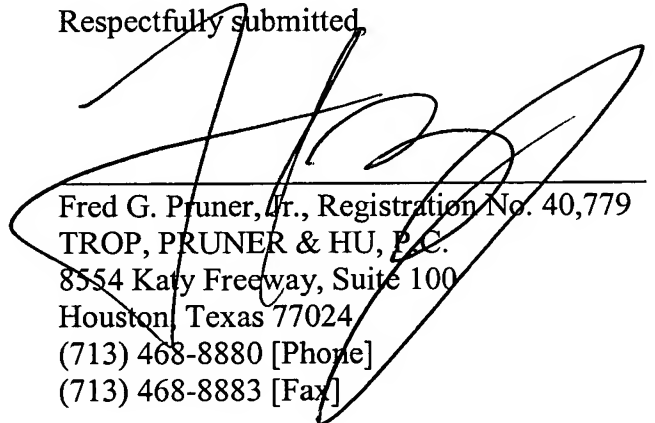
STATUS INQUIRY

Sir:

Applicants respectfully inquire as to the status of the above-identified application and would appreciate being informed as to when the Issue Notification may be expected in this application.

Date: 7/18/05

Respectfully submitted,


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Date of Deposit: July 18, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Janice Munoz